

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor: Gi-Jung Kim et al.

Group Art Unit: 2811

Serial No.: 10/601,475

Examiner: To be assigned

Filed: June 23, 2003

Confirmation No.: 7249

For: SEMICONDUCTOR WAFERS HAVING ASYMMETRIC EDGE PROFILES THAT
FACILITATE HIGH YIELD PROCESSING BY INHIBITING PARTICULATE
CONTAMINATION AND METHODS OF FORMING SAME

Date: October 21, 2003

Office of Patent Legal Administration
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

**REQUEST FOR CORRECTED FILING RECEIPT AND
PETITION TO ACCORD THE PROPER FILING DATE**

Sir:

In reviewing the Official Filing Receipt (copy attached), Applicants note that inventor Sang-Mun Chon's name incorrectly appears as Sang-Mung Chon in the above-referenced application. Applicants respectfully request that inventor Chon's name be corrected to read Sang-Mun Chon and that a corrected Filing Receipt be issued.

As set forth in 37 CFR § 1.10(c) and MPEP § 506.02, Applicants hereby petition to grant the proper filing date of June 23, 2003 rather than June 24, 2003 as this was the date the application was deposited as "Express Mail" in the U.S. Postal Service. The following is enclosed as evidence of June 23, 2003 being the correct filing date for the application, which was properly filed under 37 C.F.R. § 1.10:

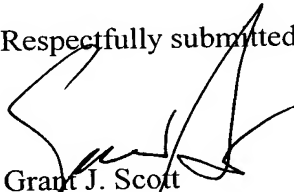
1. A true and accurate copy of the Cover Sheet for the Application showing the Express Mail signature and date on Page 2;
2. A true and accurate copy of the Express Mail Receipt along with a copy of the USPS-Track & Confirm from the web page, showing "acceptance" or deposit date with the USPS at 5:39 p.m. on June 23, 2003; and
3. A true and accurate copy of our return post card and of the official filing receipt for this application, sent by the United States Patent and Trademark Office, showing that Applicants were improperly given the filing date of June 24, 2003 instead of the Express Mail date of June 23, 2003.

In re: Gi-Jung Kim et al.
Serial No.: 10/601,475
Page 2

This petition is being filed promptly after Applicants became aware of the improperly accorded filing date. Accordingly, Applicants respectfully request the granting of June 23, 2003 as the filing date pursuant to 37 CFR § 1.10.

On the basis that Applicants properly followed the requirements of 37 CFR § 1.10 and the USPTO failed to accord the proper filing date under MPEP § 505 and thus the need to file this petition is due to error on the part of the USPTO, it is Applicant's belief that no fee is due with this petition. However, the Commissioner is authorized to charge any deficiency or credit any overpayment to Deposit Account No. 50-0220.

Respectfully submitted,

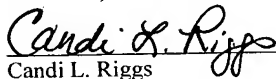


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Certificate of Mailing under 37 CFR 1.8

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Office of Patent Legal Administration, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on October 22, 2003.



Candi L. Riggs

Date of Signature: October 22, 2003



UNITED STATES PATENT AND TRADEMARK OFFICE

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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/601,475	06/24/2003 06/23/2003	2811	1170	5649-921	4	20	8

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RALEIGH, NC 27627



CONFIRMATION NO. 7249

FILING RECEIPT



OC000000011058786

Date Mailed: 10/17/2003

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. **If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).**

Applicant(s)

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Domestic Priority data as claimed by applicant

This appln claims benefit of 60/407,516 08/29/2002

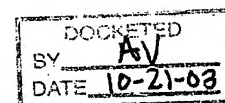
Foreign Applications

If Required, Foreign Filing License Granted: 10/17/2003

Projected Publication Date: 03/04/2004

Non-Publication Request: No

Early Publication Request: No



10-21-03 409:33 12

Title

Semiconductor wafers having asymmetric edge profiles that facilitate high yield processing by inhibiting particulate contamination and methods of forming same

Preliminary Class

257

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Title 35, United States Code, Section 184
Title 37, Code of Federal Regulations, 5.11 & 5.15**

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NOT GRANTED

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